

**Claim List:**

1-3. (Cancelled)

4. (Previously presented): A voltage regulating device for a charge pump, wherein the charge pump outputs an output voltage according to a first clock signal, a second clock signal, a third clock signal, and a fourth clock signal while the voltage regulating device comprises:

a first voltage regulating capacitor whose one terminal is coupled to an output terminal of the charge pump while the other terminal receives a first inverse clock signal, which is the complement of the first clock signal;

a second voltage regulating capacitor whose one terminal is coupled to the output terminal of the charge pump while the other terminal receives a second inverse clock signal, which is the complement of the second clock signal;

a third voltage regulating capacitor whose one terminal is coupled to the output terminal of the charge pump while the other terminal receives a third inverse clock signal, which is the complement of the third clock signal; and

a fourth voltage regulating capacitor whose one terminal is coupled to the output terminal of the charge pump while the other terminal receives a fourth inverse clock signal, which is the complement of the fourth clock signal.

5. (Previously presented): The voltage regulating device for a charge pump according to claim 4, wherein the charge pump outputs the output voltage to a load while the capacitance of the first, the second, the third and the fourth capacitor is smaller than the capacitance of the load.